



# SATHYABAMA

INSTITUTE OF SCIENCE AND TECHNOLOGY  
(DEEMED TO BE UNIVERSITY)

Accredited with "A" Grade by NAAC

Jepplaar Nagar, Rajiv Gandhi Salai, Chennai - 600 119.

Phone: 044 - 2450 3150 / 51 / 52 / 54 / 55 Fax: 044 - 2450 2344  
www.sathyabama.ac.in



## BOARD OF STUDIES

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VENUE: CENTRE FOR EXCELLENCE IN EMBEDDED AND IoT

Date: 30.03.2019

Time: 3.30 PM

### Minutes of Meeting

Dr. N. M. Nandhitha, Dean, School of EEE welcomed all members of Board of Studies. On the suggestions of various Experts, the contents are designed by the Board of Studies Members considering the various aspects and need of the society.

- Dr.T,Ravi, Head Department of Electronics and Communication started the meeting by welcoming both the external and the internal numbers to the Board of Studies meeting.
- The guidelines of the syllabus given by the management were explained by HOD to all the members of the BOS.
- The external members also ratified the decision to revise the syllabus under the mentioned guidelines.
- Initial discussion was started with course outcome and it has been assessed that COs could be modified slightly and need not be confined to six in number. It can also be reduced as per the content of syllabus. All the framed COs has to be matched with the PO for attainment. Engineering Graphics, Indian culture related more courses have to be included. Reference text books and edition has to be modified. It is decided to have the electives exclusively for school of EEE. Curriculum could be restricted to 3 pages. Include standard authors in references.
- Dr.M.Sumathi presented the changes incorporated in 'Digital Logic Circuits'. Dr M D Selvaraj. Professor, IIITDM accepted the changes suggested.
- Dr.Sivakumaran, Prof., NIT, Trichy accepted the changes proposed in 'Analog Communication' suggested by Dr.P.Chitra.



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4 units and 5<sup>th</sup> unit may be taken as case studies related to the course. This would enhance the student community to have a clear picture about the course opted.

- Dr.Sivakumaran suggested that have Machine learning technique could be added as replacement of Neural and Fuzzy Logic Systems and Deep Learning algorithms as replacement of Artificial Intelligence . Dr.T.Ravi putforth the introduction of new courses such as Machine Learning, Wireless sensor network, MEMS and its Applications, Optical Networks, Python Programing and Biomedical Signal Processing.
- Dr.T.Ravi also presented the new course introduced for PG courses are Industry 4.0 and Gateway Design for IoT Devices. The Syllabus Revision carried out in PG courses is Network Oriented OS.
- Dr.Sivakumaran also suggested that replacement of Fundamentals of Fuzzy logic and neural networks for EIE and also discussed to have two units of neural network, two units of Fuzzy logic and one unit of Genetic Algorithm. Followed by his suggestion Dr.R.Ramadevi suggested the course as Soft computing Techniques.
- Dr.V.Sivachidambaranathan, Prof.& Head, Dept. of Electrical and Electronics Engineering put forth about the Breakup of credits (15) for Project and Professional Training before the board of members.
- Professional Training 1, which is to be taken up at the end of 4th semester and before commencement of 5th semester would be allotted 2 credits. This credit would be reflected in 5th sem mark sheet.
- The Professional Training 2 as per Regulations 2015 would be modified as Minor Project, should be carried out during 6th semester. This would carry a credit of 3, which would be reflected in 6th semester mark sheet.
- The Phase 2 would be carried out in 8th semester, allotted a credit of 7, to be reflected in 8th semester marks sheet.





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- The Phase 2 would be carried out in 8th semester, allotted a credit of 7, to be reflected in 8th semester marks sheet.
- The students would have to take seminar on a turn basis every week. The seminar would be evaluated and added with CAE1 and CAE2 marks. No credits for Placement training
- The board of members appreciated the breakup credits given for professional training and Project phase
- Dr.V.Sivachidambaranathan, Prof.& Head, Dept. of Electrical and Electronics Engineering requested Mr.Barnabas, Faculty/EEE to put revision of the courses Basic Electrical and Electronics Engineering, Circuit Theory & Network Analysis. Dr.G.T.Sundar Rajan, Faculty /EEE proposed the revision of PG "Power Electronics and Drives" courses "Embedded Systems, Power Electronics in Power Systems, Special Machines and Their controller, & Modeling and Simulation in power Electronics Systems". The new syllabus presented before the board and discussed the valid additions made in the syllabus.
- Dr.V.Sivachidambaranathan, Prof.& Head, Dept. of Electrical and Electronics Engineering requested Dr.Rameshbabu, Faculty/EEE to put forth the syllabus of the new courses, 'Embedded Systems and IoT' for the approval of the board. Dr. A. Amalin Prince approved the Syllabus for this new course.

The following internal members were present in the meeting.

### INTERNAL MEMBERS

### SIGNATURE

1. Dr.N.M.Nandhitha

2. Dr.T.Ravi

3. Dr.V.Sivachidambaranathan

4. Dr.V.Vijaya Baskar

5. Dr.M.Sumathy

Handwritten signatures corresponding to the list of internal members, including a signature for Dr. M. Sumathy at the bottom right.



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6. Dr.S.Lakshmi *S. Lakshmi*
7. Dr.P.Chitra *P. Chitra*
8. Dr. P. Kavipriya *P. Kavipriya*
9. Dr.S.Barani *S. Barani*
10. Mrs.K.Srilatha *K. Srilatha*
11. Mrs.L.Magthelin Therase *L. Magthelin Therase*
12. Mrs.S.Yogalakshmi *S. Yogalakshmi*
13. Mrs.K.Sujatha *K. Sujatha*
14. Dr.R.Ramadevi *R. Ramadevi*
15. Dr.Susitra *Susitra*

## External Members

**Dr.N.Shivakumaran**

Professor  
Dept. of I& C  
NIT, Trichy.

*N. Shivakumaran*  
28/3/19.

**Dr.M.D.Selvaraj**

Associate Professor  
IIITDM, kancheepuram.

*M. D. Selvaraj*  
30/3/19.



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## BOARD OF STUDIES

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VENUE: CENTRE FOR EXCELLENCE IN EMBEDDED AND IoT

Date: 30.11.2019

Time: 3.30 PM

#### Minutes of Meeting

- Dr. N. M. Nandhitha, Dean, School of EEE welcomed all members of Board of Studies. On the suggestions of various Experts, the contents are designed by the Board of Studies Members considering the various aspects and need of the society. The meeting was convened by the Dr.N.Shivakumaran, Prof / NIT, Trichy. The guidelines of the syllabus given by the management were explained by HOD to all the members of the BOS. The external members also ratified the decision to revise the syllabus under the mentioned guidelines. The Board discussed the matters as per the agenda.
- Dr M D Selvaraj. Professor, IIITDM accepted the changes suggested by Dr P.Chitra in the subject' Digital communication. Dr.N.Shivakumaran also accepted the inclusion of error control coding in the course.
- Dr.T.Ravi discusses the syllabus of Machine learning, Industry 4.0 are discussed with the members.
- Dr.N.Shivakumaran Suggested to include the following topics in Signal and System course such as modelling of signal, speech signal processing, analysis and modelling of system and application of Fourier Transform.
- Dr.T.Ravi also presented the new course introduced for PG courses are Advanced Wireless Sensor Networks and Security in IoT.
- The Syllabus Revision carried out in PG courses is SCADA System Application.
- Dr M D Selvaraj suggested to Remove first unit in Digital Signal Processing course and include DSP processor in the fifth unit. Since first unit is the review of signals and system and to include industrial topic. Include Signal Conditioning, generation and detection for real time applications, DSP Hardwares, FPGA, ARM Processor with DSP Extension in Digital Signal Processing.



- Dr M D Selvaraj Suggested to update the fifth unit of programming in HDL with FPGA Design Flow , Architecture of Xilinx Artix7 FPGA , Configurable Logic Blocks (CLB),
- Dr.N.Shivakumaran Suggested to include applications in FPGA and Case Study: study of protocols I2C, SPI and UART in microprocessor and microcontroller course.
- Dr.T.Ravi put forth the new courses introduced such as Artificial Intelligence, Rf IC Design, Soft Computing Techniques and Cryptography and Network security
- Dr.R.Ramadevi proposed the introduction of new Elective 'Instrumentation in Aerospace and Navigation'. Dr.Sivakumaran suggested to incorporate more topics related to instrumentation.
- Dr.V.Sivachidambaranathan, Prof.& Head, Dept. of Electrical and Electronics Engineering requested Dr.Susitra, Faculty/EEE to put forth revision of the course DC Machines and Transformer and Digital Signal Processing and its Applications. The new syllabus presented before the board and discussed the valid additions made in the syllabus.
- Dr.V.Sivachidambaranathan, Prof.& Head, Dept. of Electrical and Electronics Engineering requested Dr.Rameshbabu, Faculty/EEE to put forth the syllabus of the new courses, 'Computer Aided Electrical Drawing' for the approval of the board. Dr. A. Amalin Prince approved the Syllabus for this new course.

The next BOS meeting may be scheduled on April 2020 to review and finalize the syllabus revision process.

Faculty Head, Dr. T.Ravi proposed vote of thanks and the Board of Meeting ended by 4.30 PM on 30.11.2019.

The following internal members were present in the meeting.

**INTERNAL MEMBERS**

1. Dr.N.M.Nandhitha



2. Dr.T.Ravi



SIGNATURE

3. Dr.V.Sivachidambaranathan *Dr. V. Sivachidambaranathan*
4. Dr.V.Vijaya Baskar *Vijaya Baskar*
5. Dr.M.Sumathy *MS*
6. Dr.S.Lakshmi *S. Lakshmi*
7. Dr.P.Chitra *Chitra*
8. Dr. P. Kavipriya *P. Kavipriya*
9. Dr.S.Barani *S. Barani*
10. Mrs.K.Srilatha *K. Srilatha*
11. Mrs.L.Magthelin Therase *L. Magthelin Therase*
12. Mrs.S.Yogalakshmi *S. Yogalakshmi*
13. Mrs.K.Sujatha *K. Sujatha*
14. Dr.R.Ramadevi *R. Ramadevi*
15. Dr.Susitra *Susitra*

**External Members**

**Dr.N.Shivakumaran**  
Professor  
Dept. of I& C  
NIT, Trichy.

*Dr. N. Shivakumaran*  
30/11/19

**Dr.M.D.Selvaraj**  
Associate Professor  
IIITDM, kancheepuram.

*Dr. M. D. Selvaraj*  
30/11/19